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| SERIAL NUMBER 10/720,585 | FILING DATE 11/24/2003 RULE | CLASS 712 | GROUP ART UNIT 2181 | ATTORNEY DOCKET NO. YOR920030405US1 |
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** CONTINUING DATA *****

NONE ☒

** FOREIGN APPLICATIONS *****

NONE ☒

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 02/23/2004

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|--|--|-------------------------------|-----------------------|----------------------------|
| Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no | STATE OR COUNTRY CT | SHEETS DRAWING 3 | TOTAL CLAIMS 44 | INDEPENDENT CLAIMS 3 |
| 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after allowance <input checked="" type="checkbox"/> | | | | |
| Verified and Acknowledged | Examiner's Signature <i>[Signature]</i> | Initials <i>[Initials]</i> | | |

ADDRESS

29683

HARRINGTON & SMITH, LLP

4 RESEARCH DRIVE

SHELTON, CT

06484-6212

TITLE

Method and apparatus to extend the number of instruction bits in processors with fixed length instructions, in a manner compatible with existing code

☐ All Fees☐ 1.16 Fees (Filing)